

REMARKS

Claims 1, 2, 4-10, and 17-20 are presently pending. Claims 3 and 5 are cancelled without prejudice. Claims 11-16 are withdrawn. Claim 21 is added. Claims 1, 2, 4, 10, and 20 are rejected. Claims 5-9 are indicated allowable, but objected to for dependency on a rejected base claim. Applicants appreciate the Examiner's indication of allowable subject matter.

Claims 1, 2, 7, 17, and 18 have been amended. Reconsideration is respectfully requested in view of the foregoing amendments and following remarks.

Examiner indicated in the Office Action of November 29, 2004, that the references of record do not teach or suggest the limitations of claim 5, nor would it be obvious to modify those references to include such limitation. Accordingly, claim 2 has been amended to include the limitations of claim 5. Accordingly, Examiner is requested to withdraw the rejection to claim 2 under 35 U.S.C. 102 as anticipated by Lee, and under 35 U.S.C. 103 as obvious from Lee in view of Jeddeloh.

Applicants have amended claim 1 to recite, among other limitations, "an AND gate for transmitting the signal to the memory controller after a time interval, wherein during the time interval, the AND gate receives the signal from the memory controller and the signal from the memory module" and claim 17 to recite, among other limitations, "an AND gate connected to the memory controller, said AND gate operable to transmit the another signal to the memory controller after a time interval, wherein during the time interval, the AND gate receives the signal from the memory controller and the signal from the memory module". It is

respectfully submitted that claims 1 and 17 as now amended stand in a condition for allowance.

CONCLUSION

For the foregoing reasons, each of the pending claims 1, 2, and 17 are allowable, as well as dependent claims 4, 6-10, and 18-21. Accordingly, the present application is in a condition for allowance and a notice of allowance is respectfully requested.

RESPECTFULLY SUBMITTED



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